AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116 – EXPEDITED PROCEDURE

Serial Number: 09/434,082

viting Date: November 5, 1999

PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS

AND A BIDIRECTIONAL DATA BUS

## **Double Patenting Rejection**

Claims 5-8 and 29-71 were rejected under the judicially created doctrine of double patenting over claims 1-4, 26-28, and 32-57 of U.S. Patent No. 6,286,062. Applicant herewith files a Terminal Disclaimer to remove this rejection.

FEB 2.4 2004

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## Formal Drawings

Technology Center 2100

Applicant herewith submits a new set of formal drawings. Approval and entry of formal Figures 1-6 is respectfully requested.

## **CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 349-9587 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

KEVIN J. RYAN

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9587

Date 17 Fell '04

Timothy B Clise Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 17th day of February, 2004.

Tion Kalaut

Signature

Name